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Erfinder: TSUCHIYA NORIAKI

YAMASHITA NORIFUMI

Anmelder: FUJI XEROX CO LTD

Titel: DATA PROCESSOR

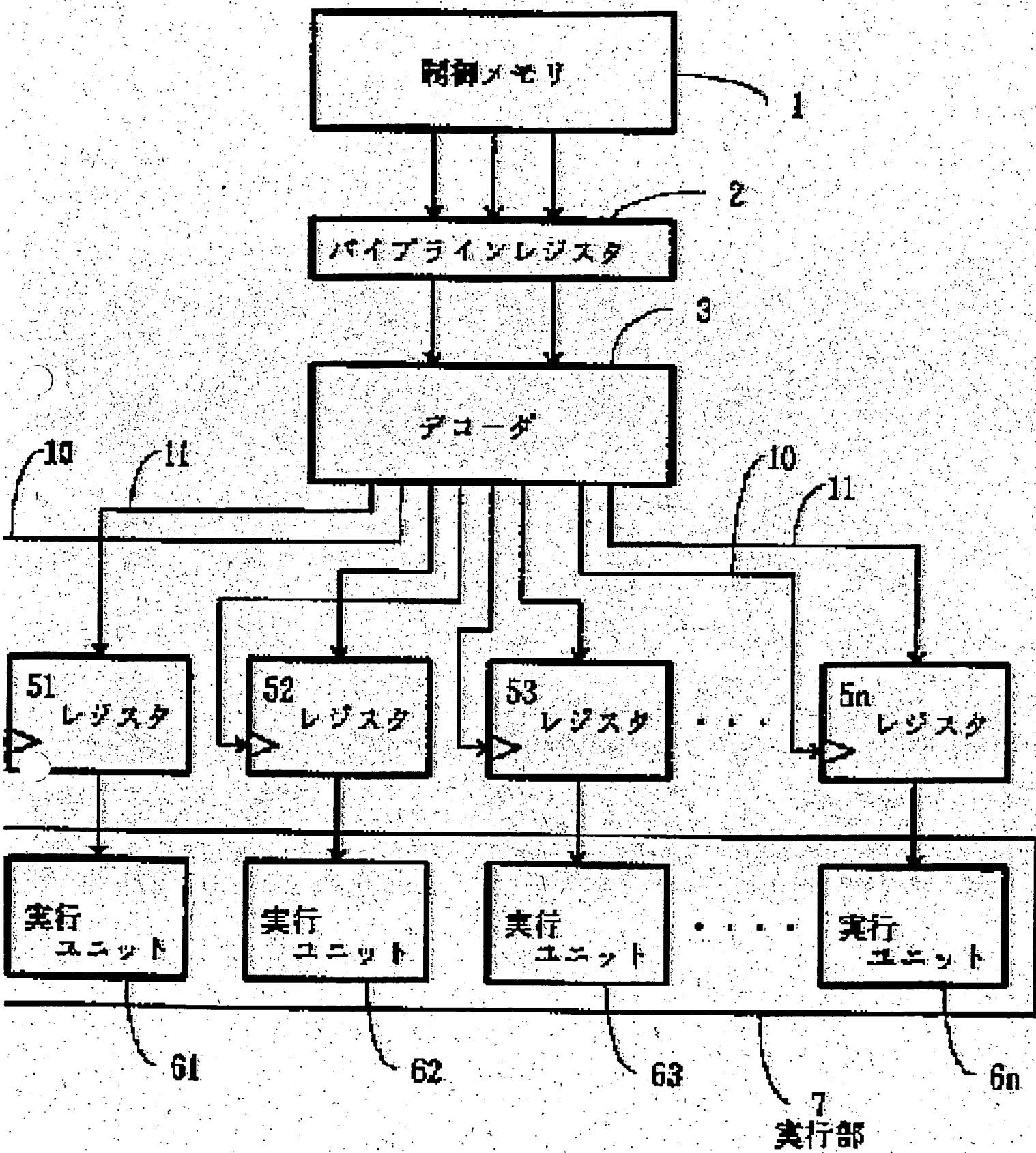
Zusammenfassung

PURPOSE: To easily perform microprogramming without increasing the machine cycle time of a pipeline system data processor.

CONSTITUTION: Registers 51-5n where the decoding output of a control word read out of a control memory 1 are provided in front of an execution part 7 corresponding to all execution units 61-6n in the execution part and the outputs of the registers 51-5n are used as control signals to the execution part 7, so that the control signals sent to the execution units 61-6n are held until next control. Consequently, even if many execution units are present in the execution part 7, the machine cycle time does not become long.

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